

FIG. 5

60

		70		70		70		70		70		70		70		70		70		70				
		TOP	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F						
64	68	0	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	62					
	68	1	10	11	12	13	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F						
	68	2	20	21	22	23	24	25	26	27	28	29	2A	2B	2C	2D	2E	2F						
	68	3	30	31	32	33	34	35	36	37	38	39	3A	3B	3C	3D	3E	3F				76		
	68	4	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F						
	68	5	50	51	52	53	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F						
	68	6	60	61	62	63	64	65	66	67	68	69	6A	6B	6C	6D	6E	6F						
	68	7	70	71	72	73	74	75	76	77	78	79	7A	7B	7C	7D	7E	7F				62		
66		BOT	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F						
	68	0	80	81	82	83	84	85	86	87	88	89	8A	8B	8C	8D	8E	8F	76					
	68	1	90	91	92	93	94	95	96	97	98	99	9A	9B	9C	9D	9E	9F						
	68	2	A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	AA	AB	AC	AD	AE	AF						
	68	3	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	BA	BB	BC	BD	BE	BF						
	68	4	C0	C1	C2	C3	C4	C5	C6	C7	C8	C9	CA	CB	CC	CD	CE	CF						
	68	5	D0	D1	D2	D3	D4	D5	D6	D7	D8	D9	DA	DB	DC	DD	DE	DF						
	68	6	E0	E1	E2	E3	E4	E5	E6	E7	E8	E9	EA	EB	EC	ED	EE	EF						
68	7	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9	FA	FB	FC	FD	FE	FF							

76
62
62

FIG. 6

METHOD AND SYSTEM FOR EFFICIENT USE
OF A MULTI-DIMENSIONAL SHARING
VECTOR IN A COMPUTER SYSTEM

U.S. Serial No. 09/910,630

Inventor: Huffman, William A.

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Express Mail No. EL759109483US

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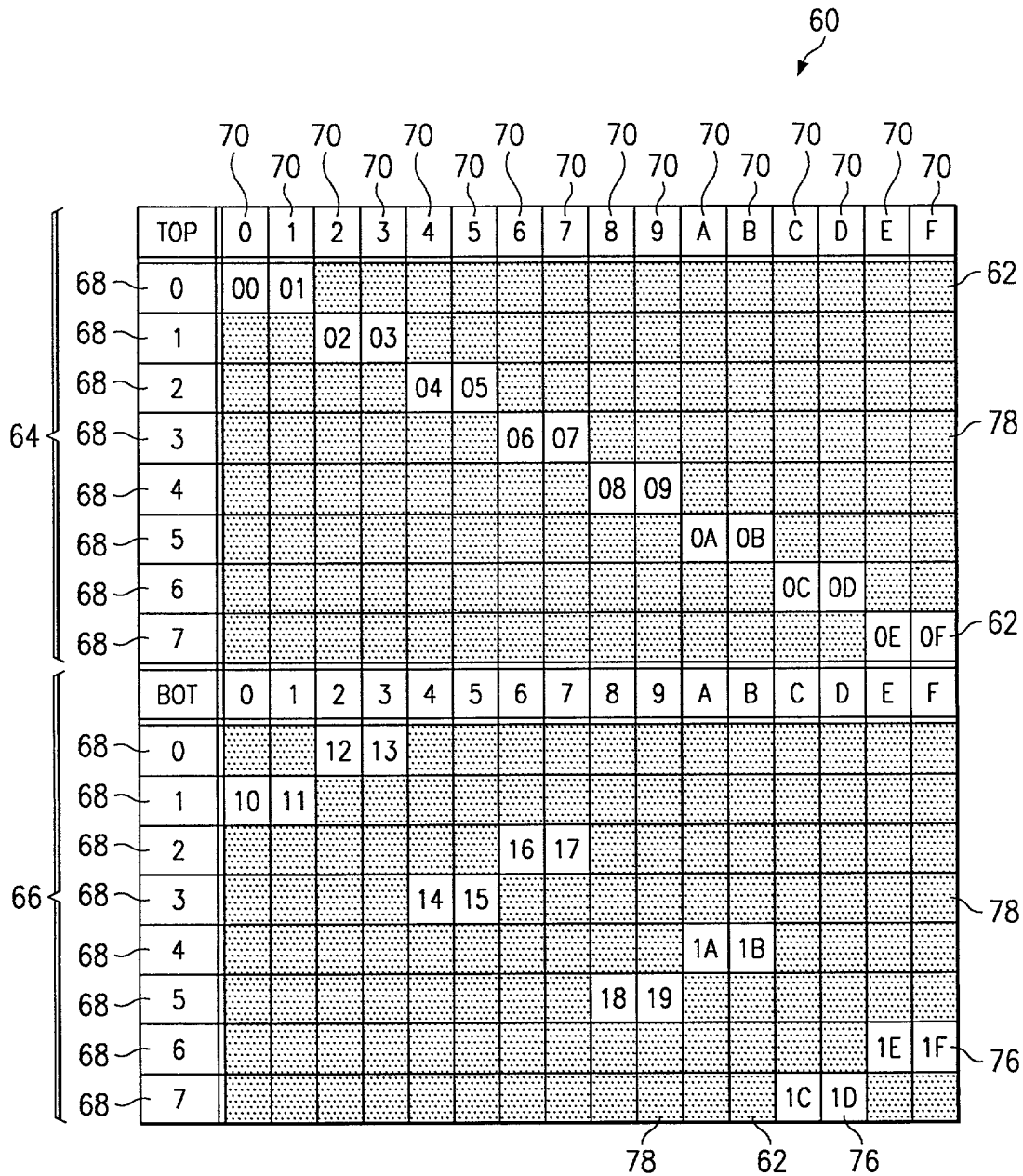


FIG. 7

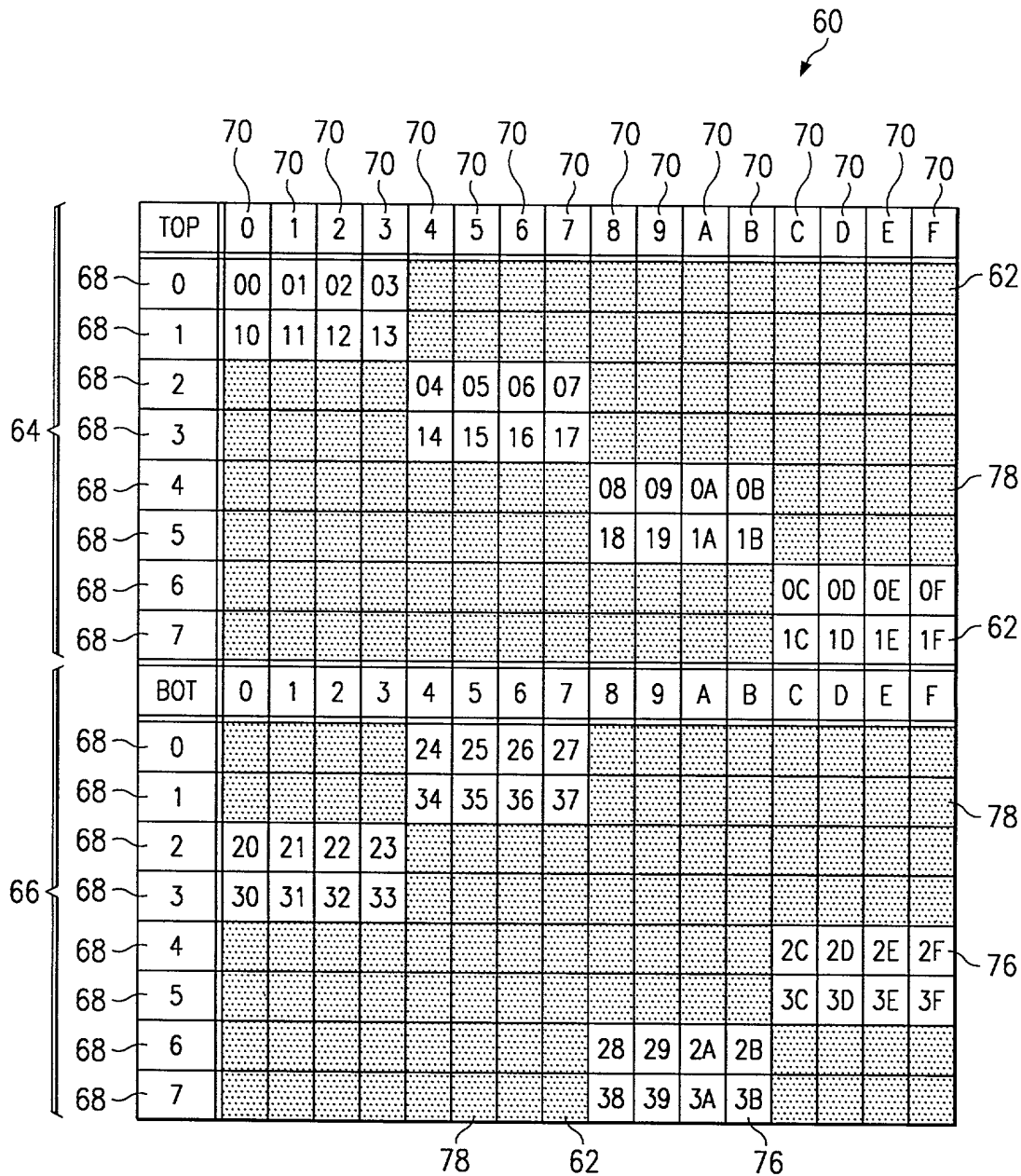


FIG. 8

60

		70	70	70	70	70	70	70	70	70	70	70	70	70	70	70	70
	TOP	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
68	0	00	01	02	03	04	05	06	07								
68	1	10	11	12	13	14	15	16	17								
68	2	20	21	22	23	24	25	26	27								
64	68	3	30	31	32	33	34	35	36	37							
68	4									08	09	0A	0B	0C	0D	0E	0F
68	5									18	19	1A	1B	1C	1D	1E	1F
68	6									28	29	2A	2B	2C	2D	2E	2F
68	7									38	39	3A	3B	3C	3D	3E	3F
	BOT	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
68	0									48	49	4A	4B	4C	4D	4E	4F
68	1									58	59	5A	5B	5C	5D	5E	5F
68	2									68	69	6A	6B	6C	6D	6E	6F
66	68	3								78	79	7A	7B	7C	7D	7E	7F
68	4	40	41	42	43	44	45	46	47								
68	5	50	51	52	53	54	55	56	57								
68	6	60	61	62	63	64	65	66	67								
68	7	70	71	72	73	74	75	76	77								

76 78 62

FIG. 9

FIG. 10

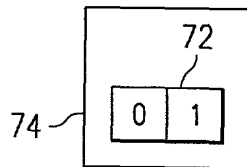


FIG. 11

NODE	HEX NOTATION	CORRESPONDING N BIT SETTINGS		
			SECOND LEAST SIGNIFICANT BIT	LEAST SIGNIFICANT BIT
		7	6 5 4	3 2 1 0
0	0x00	0	0 0 0	0 0 0 0
19	0x19	0	0 0 1	1 0 0 1

FIG. 12

NODE	SHARING MODE 11					
	PLANE N7	ROW N6,N5,N4	COLUMN N3:N0	PLANE	ROW	COLUMN
0	0	0 0 0	0 0 0 0	0	0	0
19	0	0 0 1	1 0 0 1	0	1	9

FIG. 13

NODE	SHARING MODE 10					
	PLANE N6	ROW N6⊕N3,N5,N4	COLUMN N3:N0	PLANE	ROW	COLUMN
0	0	0 0 0	0 0 0 0	0	0	0
19	0	1 0 1	1 0 0 1	0	5	9